

# **Product Overview**

The QFX5240 Switches meet the advanced Al data center networking requirements of large-scale clusters. QFX5240 switches work with the automation—such as Juniper Apstra—to assure daily operation in Al and ML workload training and access. OFX5240 Switches:

- deliver high-density <u>800GbE</u>
   ports on a fixed form factor with software to provide advanced network services tuned to the specific needs of Al/ML
- are a foundation of AI networks, and their low latencies ensure fast job completion time (JCT) to speed training through high GPLI utilization
- help teams managing AI/ML environments realize improved economics

# **QFX5240 SWITCHES DATASHEET**

# **Product Description**

Continuous evolution of AI/ML technology along with new applications are driving the next major shift in bandwidth requirement within the <u>data center fabric</u>. Juniper Networks® QFX5240 800GbE switch (64 port 800GbE) is a next-generation, fixed-configuration platform designed for spine, leaf, and border switch roles. The switch provides flexible, cost-effective, high-density 800GbE, 400GbE, 100GbE, and 50GbE interfaces for intra-IP fabric connectivity as well as higher density 200/400GbE NIC connectivity for AI/ML use cases. It's 51.2Tbps unidirectional throughput meets the bandwidth requirement of AI/ML workloads and storage systems with latency in the range of 700-750ns (store and forward). Remote Direct Memory Access (RDMA) is the de-facto data transfer technology used in AI/ML workloads, and it uses Remote Direct Memory Access over Converged Ethernet v2 (ROCEv2) for transport at the network layer. QFX5240 supports ROCEv2 along with congestion management features like Priority Flow Control (PFC), explicit congestion notification (ECN), and data center quantized congestion notification (DCQCN).

The QFX5240 helps reduce the number of network nodes deployed—decreasing the total power consumption of the data center fabric and improving the carbon footprint of the data center. These improvements are possible by having different breakout options like 128x400GbE, 256x200GbE, and 320x100GbE.

Table 1: QFX5240 Product Highlights

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|-------------------------------------|--|--|--|
|                                     |  |  |  |
| Al Data Center                      | Leaf/spine in AI/ML cluster     ROCEv2 for AI/ML workloads     DCQCN-PFC, ECN for congestion management     Support for PFC watchdog for storm avoidance     Dynamic load balancing (DLB) for better load balancing     Configurable hash-bucket size to suit different flow scale     Selective-DLB - enable/disable DLB based on opcode/byte match |  |  |
| Cloud-ready Data Center             | <ul> <li>Leaf/spine in IP fabric</li> <li>Leaf/spine/super spine in EVPN-VXLAN fabric</li> <li>Support for EVPN-VXLAN</li> <li>136K MAC scale</li> <li>860K IPv4 route scale</li> </ul>  |  |  |
| Port Options                        | <ul> <li>64 ports of 800GbE</li> <li>128 ports of 400GbE (achieved with breakout cable)</li> <li>256 ports of 200GbE (achieved with breakout cable)</li> <li>320 ports of 100GbE (achieved with breakout cable)</li> </ul>   |  |  |
| Platform Parameters                 | <ul> <li>Throughput: 51.2Tbps unidirectional</li> <li>Buffer: 165MB</li> <li>Tool less rack mount kit</li> <li>Hot swappable power supplies and FAN trays</li> <li>Power supply redundancy</li> <li>Remote power cycling capability</li> </ul>   |  |  |

# Features and Benefits

# AI/ML Design

Artificial intelligence puts new challenges on compute, network, and storage solutions with large models that run in parallel across many GPUs for training. These models require fast job completion time (JCT) with minimal delays for the last GPU to finish its calculations, that is, low tail latency. Architects optimize the cluster performance through rail-optimized design (Read this <u>Juniper White Paper</u> for more information about AI/ML cluster design). As model sizes and datasets continue to grow, designs must accommodate more GPUs in the cluster, requiring that the network seamlessly scale, without compromising performance, or introducing communication bottlenecks.

The QFX5240 meets the needs of these large-scale Al networks. The switch provides:

- 64 ports of 800GbE on a 2 U switch to reduce costs on both space and total power utilization
- Choice of connectivity with both OSFP and QSFP-DD variants of 800GbE for leaf-spine connectivity
- Advanced telemetry capabilities to support ECN/PFC counters
- Fine-grained, load-balancing capability to handle reduced flow entropy
- Automation of rail-optimized design through Apstra

## Automation

Automation tools, such as Apstra, ensure the reliable set up of expansive networks with ongoing verification of the deployment along with monitoring of operations. Apstra <a href="intent-based">intent-based</a> <a href="networking">networking</a> delivers full Day 0 through Day 2+ capabilities for IP/EVPN fabrics with closed-loop assurance in the data center. Apstra provides a broad set of operational capabilities, with multiple built-in intent-based analytics probes, flow visibility, and analysis to ensure that the AI network is running as designed. Apstra provides a simple UI workflow to create custom intent-based analytics to capture, enrich, and visualize data from the AI network.

## Monitoring

The QFX5240 supports <u>Junos</u> telemetry interface, a modern telemetry streaming tool that provides performance monitoring in complex, dynamic data centers. Streaming data to a performance management system lets network administrators measure trends in link and node utilization and troubleshoot issues such as network congestion in real time.

Junos telemetry interface provides:

- Application visibility and performance management by provisioning sensors to collect and stream data and analyze the application and workload flow path through the network
- Capacity planning and optimization by proactively detecting hotspots and monitoring latency and microbursts
- Troubleshooting and root cause analysis via high-frequency monitoring and correlating overlay and underlay networks

Additionally, the <u>Junos Evolved operating system</u> supports a robust API set to support automation through Terraform, Ansible, zerotouch provisioning (ZTP), operations and event scripts, automatic rollback, and Python scripts.



QFX5240-64OD

# **Specifications**

# **Hardware Specifications**

Table 2: QFX5240 System Capacity

| Parameter               | QFX5240-64OD  | QFX5240-64QD  |  |  |
|-------------------------|---|---|--|--|
| System throughput       | 51.2/102.4 Tbps uni/bidirectional   | 51.2/102.4 Tbps uni/bidirectional   |  |  |
| Max Forwarding<br>Rate  | 21.2Bpps  | 21.2Bpps  |  |  |
| Port density            | 64 ports of OSFP 800GbE   | 64 ports of QSFP-DD 800GbE  |  |  |
| Max ports with breakout | 64 × 800GbE, 128 × 400GbE, 320<br>× 100GbE                                | 64 × 800GbE, 128 × 400GbE, 320<br>× 100GbE                                |  |  |
| Dimensions (W x H x D)  | 17.26 x 3.46 x 25.52 in (43.8 x 8.8<br>x 64.8 cm)                         | 17.26 x 3.46 x 25.52 in (43.8 x 8.8 x 64.8 cm)                            |  |  |
| Rack units              | 2 U   | 2 U   |  |  |
| Weight                  | 22kgs (48.50lbs) fully loaded<br>without optics                           | 22kgs (48.50lbs) fully loaded<br>without optics                           |  |  |
| Operating system        | Junos OS Evolved  | Junos OS Evolved  |  |  |
| Switch chip             | Broadcom Tomahawk5  | Broadcom Tomahawk5  |  |  |
| CPU                     | Intel Ice Lake (4 core)   | Intel Ice Lake (4 core)   |  |  |
| Memory                  | 32GB (16GBx2) of DDR4   | 32GB (16GBx2) of DDR4   |  |  |
| Storage                 | 2x480GB   | 2x480GB   |  |  |
| Power                   | Redundant (1+1) hot-pluggable<br>3000W AC (200 to 240V) power<br>supplies | Redundant (1+1) hot-pluggable<br>3000W AC (200 to 240V) power<br>supplies |  |  |
| Cooling                 | Ports-to-PSU (AFO)<br>4 hot-pluggable fan modules                         | Ports-to-PSU (AFO)<br>4 hot-pluggable fan modules                         |  |  |
| Total packet buffer     | 165 MB  | 165 MB  |  |  |
| Warranty                | Juniper standard one-year<br>warranty                                     | Juniper standard one-year<br>warranty                                     |  |  |

#### Table 3: QFX5240 Feature Matrix

| F |  |  |  |  |
|---|--|--|--|--|
|   |  |  |  |  |

#### Layer 2 Features

STP-IEEE 802.1D (802.1D-2004)

Rapid Spanning Tree Protocol (RSTP) (IEEE 802.1w); MSTP (IEEE 802.1s)

Bridge protocol data unit (BPDU) protect

Loop protect

Root protect

VLAN-IEEE 802.1Q VLAN trunking

Routed VLAN interface (RVI)

Static MAC address assignment for interface

Global MAC learning disable

Link Aggregation and Link Aggregation Control Protocol (LACP) (IEEE 802.3ad)

IEEE 802.1AB Link Layer Discovery Protocol (LLDP)

#### Layer 3 Features

Static routing

OSPF v2/v3

Filter-based forwarding

VRRP/VRRPv3

IPv6

Virtual routers

Loop-free alternate (LFA)

BGP

IS-IS

Dynamic Host Configuration Protocol (DHCP) v4/v6 relay (stateless)

VRF-aware DHCP

IPv4/IPv6 over GRE tunnels

#### Multicast

Internet Group Management Protocol (IGMP) v1/v2/v3

Multicast Listener Discovery (MLD) v2

IGMP proxy, querier

IGMP v1/v2/v3 snooping

Intersubnet multicast using IRB interface

MLD snooping

Protocol Independent Multicast PIM-SM, PIM-SSM, PIM-DM, PIM-Bidir Multicast Source Discovery Protocol (MSDP)

## Quality of Service (QoS)

L2 and L3 QoS: Classification, rewrite, queuing

Rate limiting:

- Ingress policing: 1 rate 2 color, 2 rate 3 color
- Egress policing: Policer, policer mark down action
- Egress shaping: Per queue, per port

10 hardware queues per port (8 unicast and 2 multicast)

Strict priority queuing (LLQ), shaped-deficit weighted round robin (SDWRR)

Layer 2 classification criteria: Interface, MAC address, Ether type, 802.1p, VLAN

Congestion avoidance capabilities: WRED, ECN

Trust IEEE 802.1p

Configurable shared buffer and buffer monitoring

Congestion Notification Profile

Priority-based flow control (PFC)-IEEE 802.1Qbb

# High Availability

Bidirectional Forwarding Detection (BFD)

### Visibility and Analytics

Switched Port Analyzer (SPAN)

| F | - 1 |  |  |
|---|-----|--|--|
|   |     |  |  |

Remote SPAN (RSPAN)

Encapsulated Remote SPAN (ERSPAN)

sFlow v5

Junos Telemetry Interface Management and Operations

Role-based CLI management and access

Junos OS Evolved configuration rescue and rollback

Image rollback

SNMP v1/v2/v3

Junos OS Evolved XML management protocol

Automation and orchestration

Zero-touch provisioning (ZTP)

Pythor

Junos OS Evolved event, commit, and OP scripts

#### Network Services

ROCEv2

DCOCN, PFC, ECN

PFC watchdog

Dynamic load balancing (DLB)

Configurable hash-bucket size

## **Environmental Ranges**

## Table 4: QFX5240-64OD and QFX5240-64QD operating parameters

| Parameter                      | QFX5240-64OD   |
|--------------------------------|--|
| Operating temperature          | 0° to 40°C @ sea level   |
| Storage temperature            | -40° to 70°C   |
| Operating altitude             | Up to 6000 ft/3962 m   |
| Relative humidity operating    | 5 to 90% non-condensing  |
| Relative humidity nonoperating | 5 to 90% non-condensing  |
| Seismic                        | Zone 4 earthquake rating<br>(GR-63 EQ zone 4)                    |
| Typical power consumption      | 932-Watt 100% traffic with DACs (without optics power) @<br>25°C |

## Electromagnetic compatibility

- FCC 47 CFR Part 15
- ICES-003 / ICES-GEN
- BS EN 55032
- BS EN 55035
- EN 300 386 V1.6.1
- EN 300 386 V2.2.1
- BS EN 300 386
- EN 55032
- CISPR 32
- EN 55035
- CISPR 35
- IEC/EN 61000 Series
- IEC/EN 61000-3-2
- IEC/EN 61000-3-3

#### QFX5240 Switches Datasheet

- AS/NZS CISPR 32
- VCCI-CISPR 32
- BSMI CNS 15936
- KS C 9835 (Old KN 35)
- KS C 9832 (Old KN 32)
- KS C 9610
- BS EN 61000 Series

## Environmental compliance

- Restriction of Hazardous Substances (RoHS)
- Toxic Substances Control Act (TSCA)
- Persistent Organic Pollutants (POPs)
- Recycled Material Waste Electronics and Electrical Equipment (WEEE)
- Registration, Evaluation, Authorization and Restriction of Chemicals (REACH)
- Substances of Concern in Products (SCIP)

## Safety

- UL 60950-1:2007 R10.14 Information Technology Equipment
- CAN/CSA-C22.2 No. 60950-1-07, Amd 1:2011, Amd 2:2014 Information Technology Equipment
- IEC 62368-1:2014 (2nd Edition) Audio/Video, Information and Communication Technology Equipment (Include all country deviation)
- IEC 62368-1:2018 (3rd Edition) Audio/Video, Information and Communication Technology Equipment (Include all country deviation)
- EN 62368-1:2014+A11:2017 Audio/Video Information and Communication Technology Equipment
- UL/CSA 62368-1:2019 (3rd Edition) Audio/Video, Information and Communication Technology Equipment
- IEC/EN 60825-1 Safety of Laser Products Part 1: Equipment classification and requirements

# **Ordering Information**

| Product SKU       | Description                                     |
|-------------------|---|
| QFX5240-640D-A0   | 64x800GE OSFP800, AC, Front-to-back airflow     |
| QFX5240-64OD-CHAS | 64x800GE OSFP800, spare chassis                 |
| QFX5240-64QD-AO   | 64x800GE QSFP-DD 800, AC, Front-to-back airflow |
| QFX5240-64QD-CHAS | 64x800GE QSFP-DD 800, spare chassis             |

## License SKUs

| S-QFX5K-C5-A1-X<br>(X=3,5,P) | Advanced 1 Software License (X Years Subscription, X=3,5, or P for Perpetual) for QFX5240-640D/QD   |
|------------------------------|---|
| S-QFX5K-C5-A2-X<br>(X=3,5,P) | Advanced 2 Software License (X Years Subscription, X=1,3,5, or P for Perpetual) for QFX5240-640D/QD |
| S-QFX5K-C5-P1-X (X=3,5,P)    | Premium Software License (X Years Subscription, X=1,3,5, or P for Perpetual) for QFX5240-64OD/QD    |

Note: The information provided is from early prototyping and may vary from the actual GA product.

# **Optics and Transceivers**

Up-to-date information on supported optics can be found on the <u>Hardware Compatibility Tool</u>. The QFX5240 line of Switches supports varying port speeds at 800GbE, 400GbE, and 100GbE, with different transceiver options.

# **Useful Links**

Feature Explorer

Hardware Compatibility Tool

Recommended Releases

# **About Juniper Networks**

Juniper Networks believes that connectivity is not the same as experiencing a great connection. Juniper's Al-Native Networking Platform is built from the ground up to leverage Al to deliver the best and most secure user experiences from the edge to the data center and cloud. Additional information can be found at Juniper Networks (<a href="https://www.juniper.net">www.juniper.net</a>) or connect with Juniper on  $\underline{X}$  (Twitter), LinkedIn, and Facebook.

**Corporate and Sales Headquarters** 

Juniper Networks, Inc.

1133 Innovation Way

Sunnyvale, CA 94089 USA

Phone: 888.JUNIPER (888.586.4737)

or +1.408.745.2000 www.juniper.net **APAC** and **EMEA** Headquarters

Juniper Networks International B.V. Boeing Avenue 240 1119 PZ Schiphol-Rijk

Amsterdam, The Netherlands

Phone: +31.207.125.700



Driven by Experience

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